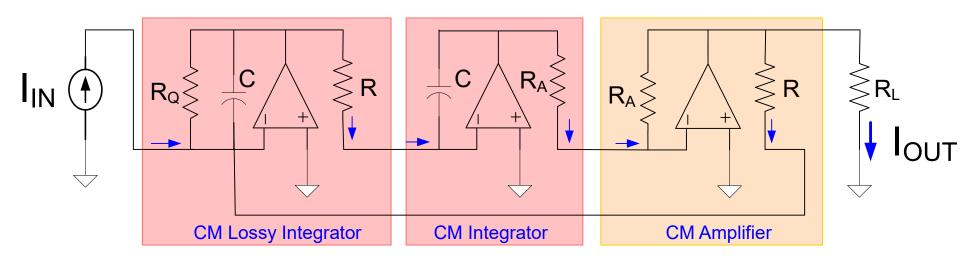
EE 508 Lecture 30

Switched-Current Integrators Leapfrog filters

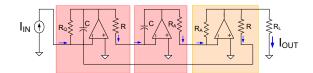
Current-Mode Two Integrator Loop

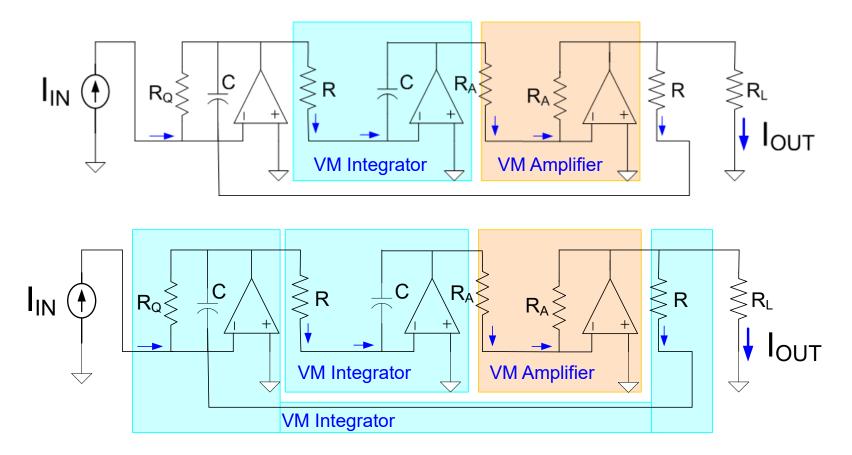


- Straightforward implementation of the two-integrator loop
- Simple structure

Current-Mode Two Integrator Loop

An Observation:





This circuit is identical to another one with two voltage-mode integrators and a voltage-mode amplifier !

Review from last time Switched-Current Filters

Basic idea introduced by Hughes and Bird at ISCAS 1989

 V_{DD}

 $I_{OUT}(nT) = AI_{IN}(nT-T)$

Cp is parasitic gate capacitance on $\rm M_{2}$

Very low power dissipation

Potential to operate at very low voltages

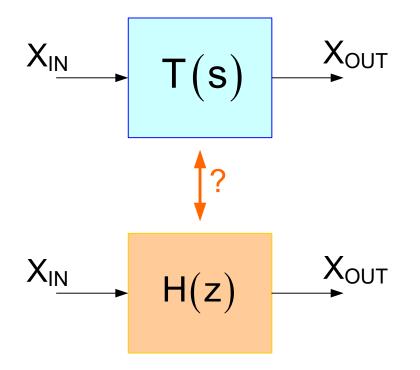
Potential for accuracy of a SC circuit at both low and high frequencies but without the Op Amp and large C ratios

Neither capacitor or resistor values needed to do filtering!

A completely new approach to designing filters that offers potential for overcoming most of the problems plaguing filter designers for decades

Before developing Switch-Current concept, need to review background information in s to z domain transformations

s-domain to z-domain transformations

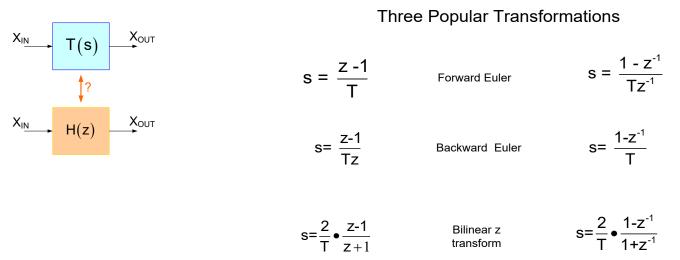


For a given T(s) would like to obtain a function H(z) or for a given H(z) would like to obtain a T(s) such that <u>preserves</u> the magnitude and phase response

Mathematically, would like to obtain the relationship:

$$T(s)|_{s=j\omega} = H(z)|_{z=e^{j\omega T}}$$

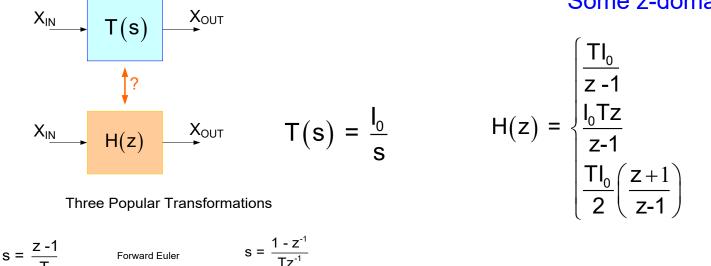
s-domain to z-domain transformations



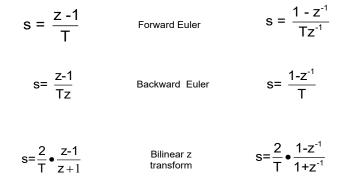
- Transformations of standard approximations in s-domain are the corresponding transformations in the z-domain
- Transformations are not unique
- Transformations cause warping of the imaginary axis and may cause change in basic shape
- Transformations do not necessarily guarantee stability
- These transformations preserve order

z-domain integrators





Three Popular Transformations



Corresponding difference equations:

$$\begin{split} V_{OUT}\left(nT+T\right) &= TI_{0}V_{IN}\left(nT\right) + V_{OUT}\left(nT\right) \\ V_{OUT}\left(nT+T\right) &= I_{0}TV_{IN}\left(nT+T\right) + V_{OUT}\left(nT\right) \\ V_{OUT}\left(nT+T\right) &= \frac{TI_{0}}{2}\left(V_{IN}\left(nT+T\right) + V_{IN}\left(nT\right)\right) + V_{OUT}\left(nT\right) \end{split}$$

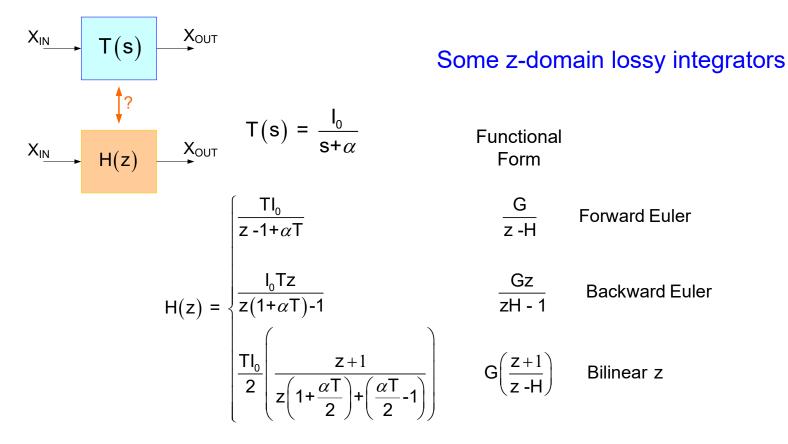
Forward Euler **Backward Euler** Bilinear z

Backward Euler

Forward Euler

Bilinear z

z-domain lossy integrators



Corresponding difference equations:

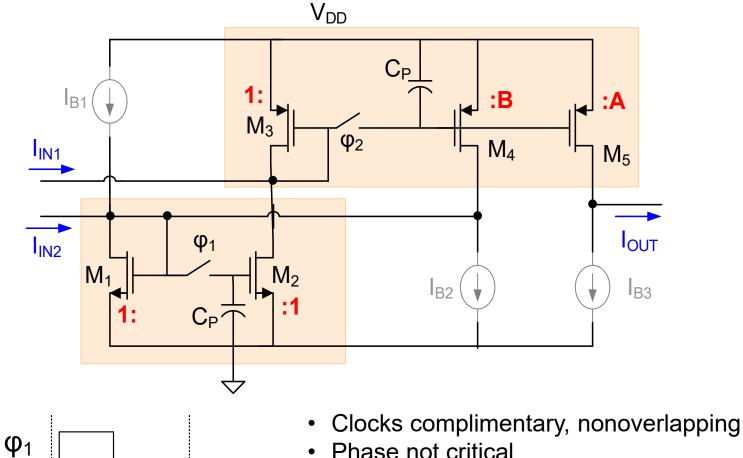
$$\begin{split} V_{\text{OUT}}\left(nT+T\right) &= GV_{\text{IN}}\left(nT\right) + HV_{\text{OUT}}\left(nT\right) & \text{Forward Euler} \\ HV_{\text{OUT}}\left(nT+T\right) &= GV_{\text{IN}}\left(nT+T\right) + V_{\text{OUT}}\left(nT\right) & \text{Backward Euler} \\ V_{\text{OUT}}\left(nT+T\right) &= G\left(V_{\text{IN}}\left(nT+T\right) + V_{\text{IN}}\left(nT\right)\right) + HV_{\text{OUT}}\left(nT\right) & \text{Bilinear z} \end{split}$$

Consider this circuit

φ₂

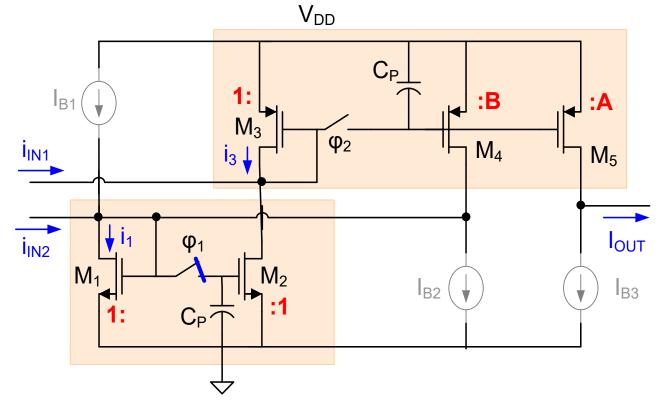
nT

(n+1)T



Phase not critical •

Assume inputs change only during phase Φ_2 (may be outputs from other like stages)



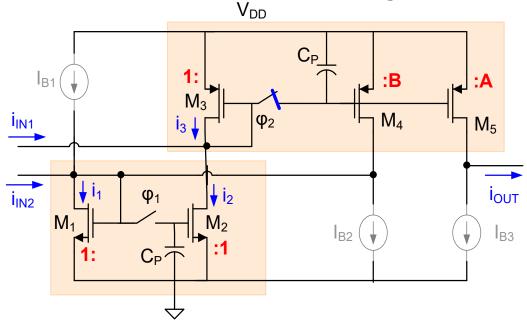
Consider Φ_1 closed, Φ_2 open (nT-T < t < nT-T/2)

$$i_{1}(t) = Bi_{3}(nT-T) + i_{iN2}(t)$$

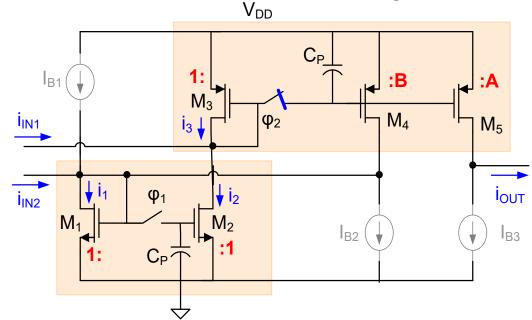
Since current does not change during this interval

$$i_1(nT-T) = Bi_3(nT-T) + i_{iN2}(nT-T)$$

Switched-Current Integrator V_{DD}



Consider
$$\Phi_2$$
 closed, Φ_1 open (nT-T/2 < t < nT)
 $i_2(t) = i_1(nT-T)$
 $i_2(t) = i_3(t) + i_{IN1}(t)$
 $i_{OUT}(t) = Ai_3(t)$
 $i_1(nT-T) = Bi_3(nT-T) + i_{IN2}(nT-T)$ (from first phase)
 $\left(\frac{1}{A}\right)i_{OUT}(t) + i_{IN1}(t) = \frac{B}{A}i_{OUT}(nT-T) + i_{IN2}(nT-T)$



Consider Φ_2 closed, Φ_1 open (nT-T/2 < t < nT)

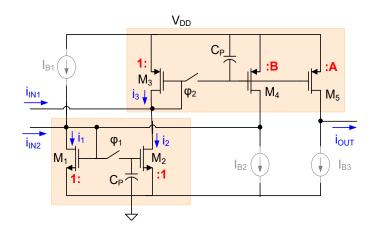
$$\left(\frac{1}{A}\right)i_{OUT}(t) + i_{IN1}(t) = \frac{B}{A}i_{OUT}(nT-T) + i_{IN2}(nT-T)$$

Evaluating at t=nT, we have

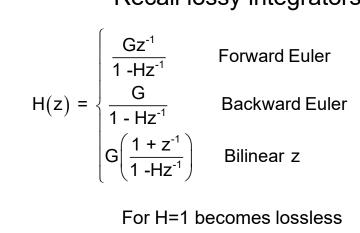
$$\left(\frac{1}{A}\right)i_{OUT}(nT) + i_{IN1}(nT) = \frac{B}{A}i_{OUT}(nT-T) + i_{IN2}(nT-T)$$

Taking z-transform, obtain

$$I_{OUT}(z) = \left(\frac{Az^{-1}}{1 - Bz^{-1}}\right) I_{IN2}(z) - \left(\frac{A}{1 - Bz^{-1}}\right) I_{IN1}(z)$$



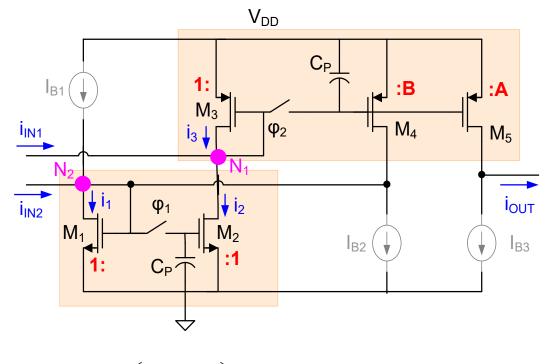
Recall lossy integrators:



For H=1 becomes lossless

$$I_{OUT}(z) = \left(\frac{Az^{-1}}{1 - Bz^{-1}}\right) I_{IN2}(z) - \left(\frac{A}{1 - Bz^{-1}}\right) I_{IN1}(z)$$

If I_{IN1}=0, becomes Forward Euler integrator If I_{N2}=0, becomes Backward Euler integrator If I_{N1} = - I_{IN2} , becomes Bilinear Integrator

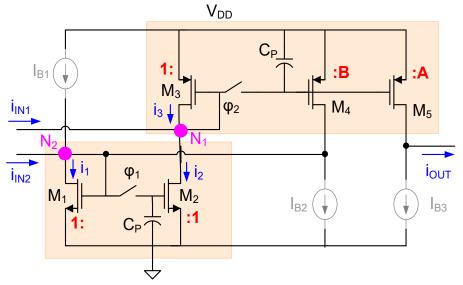


$$I_{OUT}(z) = \left(\frac{Az^{-1}}{1 - Bz^{-1}}\right) I_{IN2}(z) - \left(\frac{A}{1 - Bz^{-1}}\right) I_{IN1}(z)$$

- Summing inputs can be provided by summing currents on N₁ or N₂ or both
- Multiple outputs can be provided by adding outputs to upper mirror
- Amount of loss determined by mirror gain B



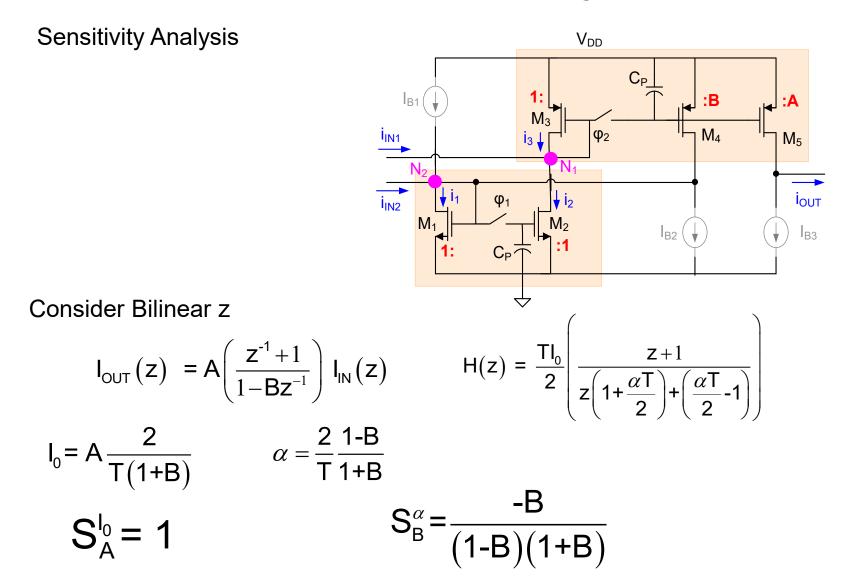




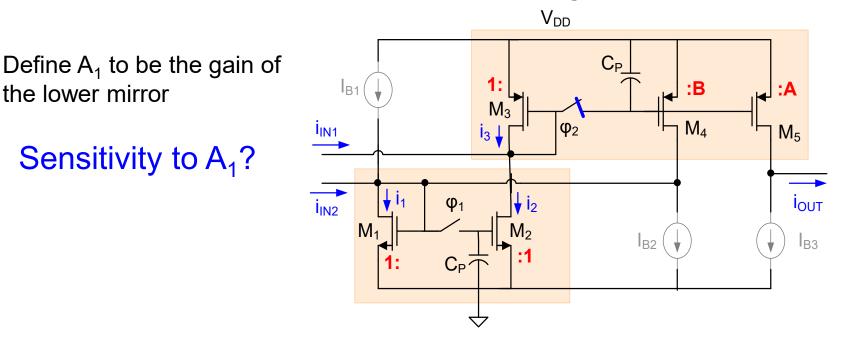
Consider Forward Euler

$$I_{OUT}(z) = \left(\frac{Az^{-1}}{1 - Bz^{-1}}\right) I_{IN2}(z) \qquad H(z) = \frac{TI_0}{z - 1 + \alpha T}$$
$$I_0 = \frac{A}{T} \qquad \frac{1 - B}{T} = \alpha$$
$$S_A^{I_0} = 1 \qquad S_B^{\alpha} = \frac{-B}{1 - B}$$

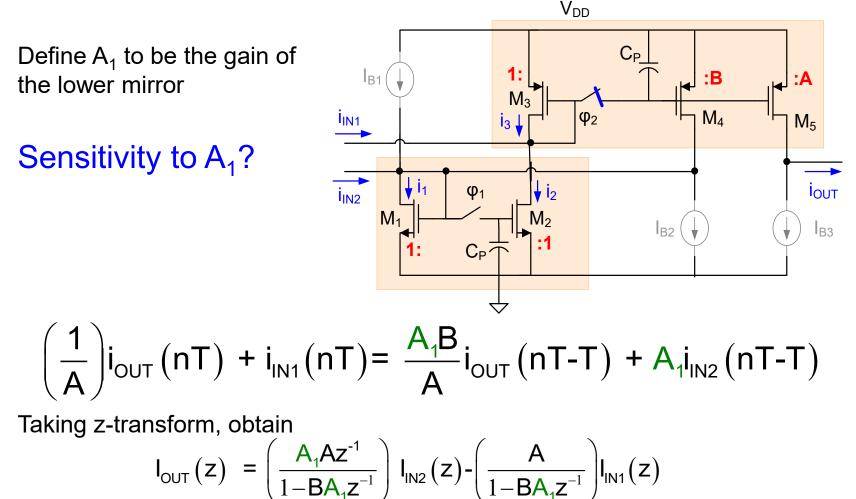
For low loss integrator (e.g. ideal integrator), the sensitivity of α is very large!



For low loss integrator (e.g. ideal integrator), the sensitivity of α is very large! What about the sensitivity to the gain of the lower current mirror?



Consider Φ_2 closed, Φ_1 open (nT-T/2 < t < nT) $i_2(t) = A_1 i_1(nT-T)$ $i_2(t) = i_3(t) + i_{IN1}(t)$ $i_{OUT}(t) = Ai_3(t)$ $i_1(nT-T) = Bi_3(nT-T) + i_{IN2}(nT-T)$ (from first phase) $\left(\frac{1}{A}\right)i_{OUT}(t) + i_{IN1}(t) = \frac{A_1B}{A}i_{OUT}(nT-T) + A_1i_{IN2}(nT-T)$



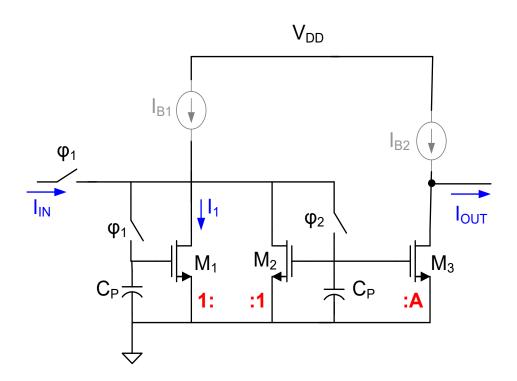
Consider Forward Euler

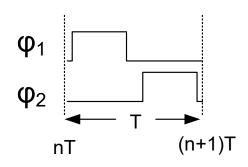
$$\frac{1-BA_1}{T} = \alpha \qquad S_B^{\alpha} = \frac{-BA_1}{1-BA_1} \qquad S_{A_1}^{\alpha} = \frac{-BA_1}{1-BA_1}$$

Sensitivity to A₁ is also large for low-loss or lossless integrator



Consider another circuit

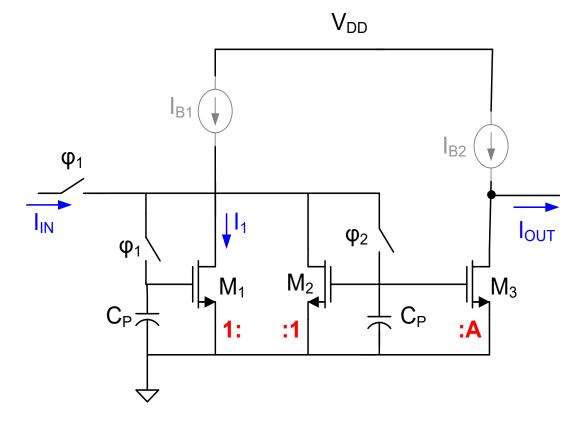


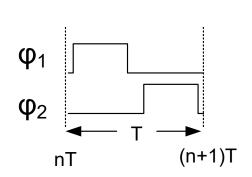


Consider Φ_1 closed, Φ_2 open (nT-T < t < nT-T/2)

$$i_{1}(t) = \frac{1}{A}i_{OUT}(nT-T) + i_{iN}(t)$$

$$i_{1}(nT-T) = \frac{1}{A}i_{OUT}(nT-T) + i_{iN}(nT-T)$$
(1)



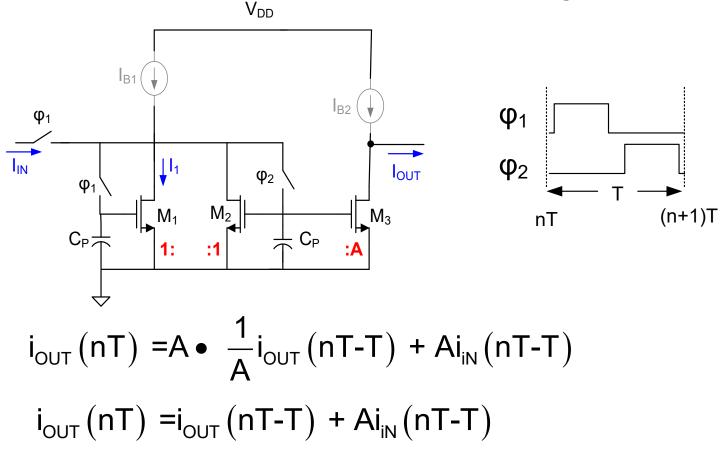


Consider Φ_2 closed, Φ_1 open (nT-T/2 < t < nT)

$$i_{OUT}(t) = Ai_{1}(nT-T)$$
$$i_{OUT}(nT) = Ai_{1}(nT-T) \qquad (2)$$

combining (1) and (2), obtain

$$i_{OUT}(nT) = A \bullet \frac{1}{A}i_{OUT}(nT-T) + Ai_{iN}(nT-T)$$

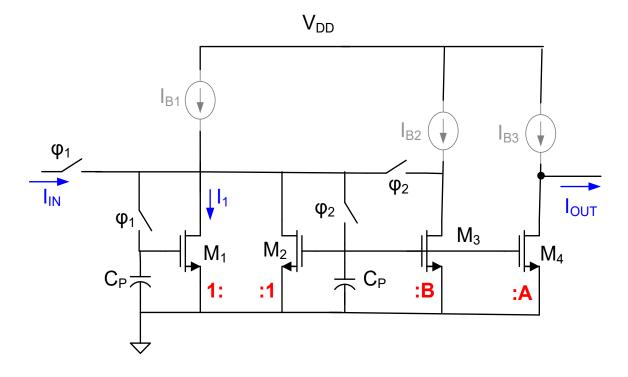


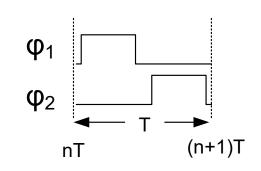
Taking z-transform, obtain

$$\mathbf{I}_{\mathsf{OUT}}(\mathbf{z}) = \left(\frac{\mathbf{A}\mathbf{z}^{-1}}{1 - \mathbf{z}^{-1}}\right) \mathbf{I}_{\mathsf{IN}}(\mathbf{z})$$

Forward Euler Integrator

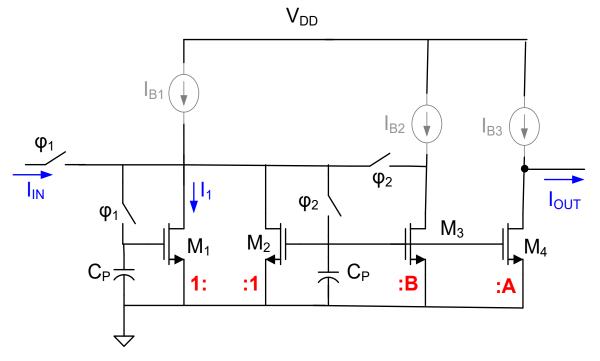
- Lossless Integrator (no matching required!)
- Matching of M₁ and M₂ not required
- Gain A does not affect coefficient of z⁻¹ in the denominator

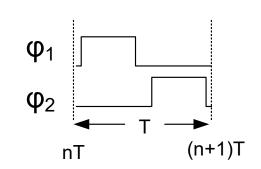




Consider Φ_1 closed, Φ_2 open (nT-T < t < nT-T/2)

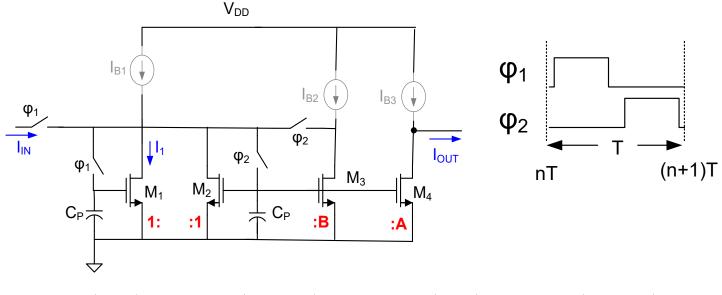
$$i_{1}(t) = \frac{1}{A}i_{OUT}(nT-T) + i_{iN}(t)$$
$$i_{1}(nT-T) = \frac{1}{A}i_{OUT}(nT-T) + i_{iN}(nT-T)$$
(1)





Consider Φ_2 closed, Φ_1 open (nT-T/2 < t < nT)

$$\begin{split} i_{OUT}(t) &= A \bigg(i_1 (nT-T) - \frac{B}{A} i_{OUT}(t) \bigg) \\ i_{OUT}(nT) &= A \bigg(i_1 (nT-T) - \frac{B}{A} i_{OUT}(nT) \bigg) \end{split}$$
(2) combining (1) and (2), obtain
$$i_{OUT}(nT) &= i_{OUT}(nT-T) - B i_{OUT}(nT) + A i_{IN}(nT-T) \bigg) \end{split}$$



$$i_{OUT}(nT) = i_{OUT}(nT-T) - Bi_{OUT}(nT) + Ai_{IN}(nT-T)$$

Taking z-transform, obtain

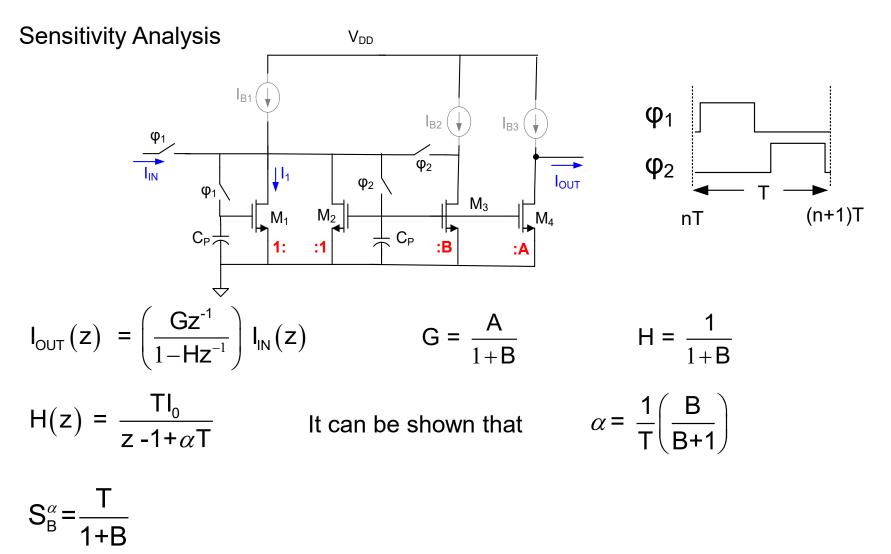
$$I_{OUT}(z) = \left(\frac{Gz^{-1}}{1 - Hz^{-1}}\right) I_{IN}(z)$$

ere
$$G = \frac{A}{1 + B} \qquad H = \frac{1}{1 + B}$$

where

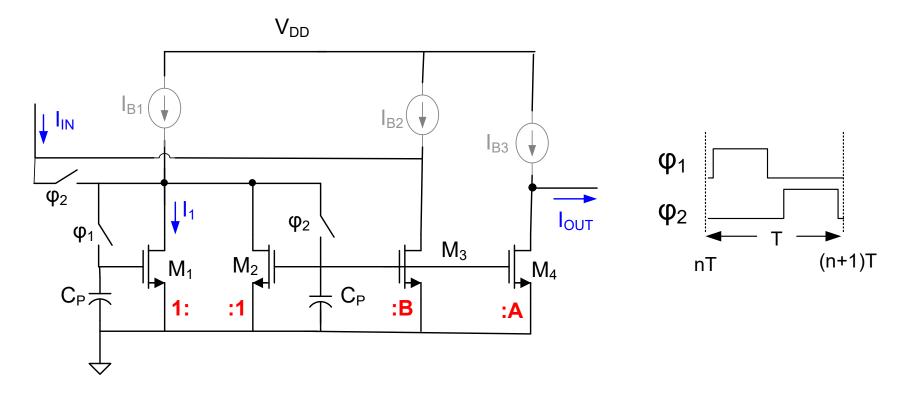
- Lossy Integrator
- Matching of M₁ and M₂ not required
- Gain A does not affect coefficient of z⁻¹ in the denominator

Forward Euler Integrator (Lossy)



For small loss, B is small and so is the sensitivity

Another structure

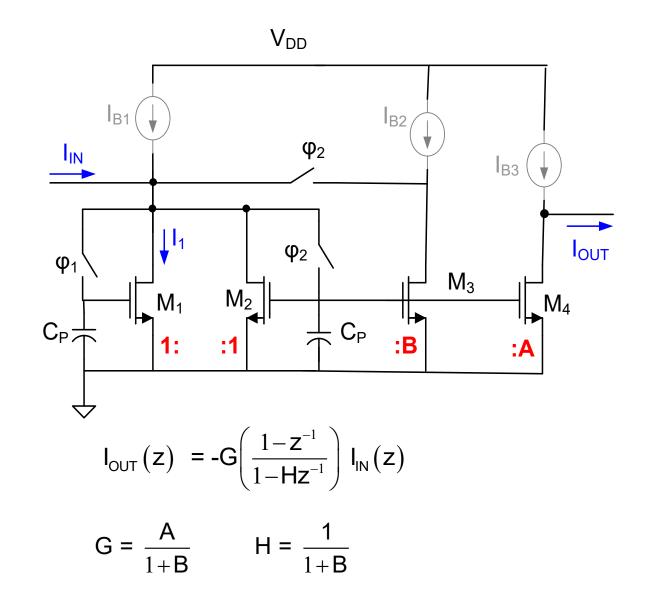


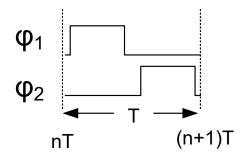
 $I_{\text{out}}(z) = \left(\frac{-G}{1 - Hz^{-1}}\right) I_{\text{in}}(z)$

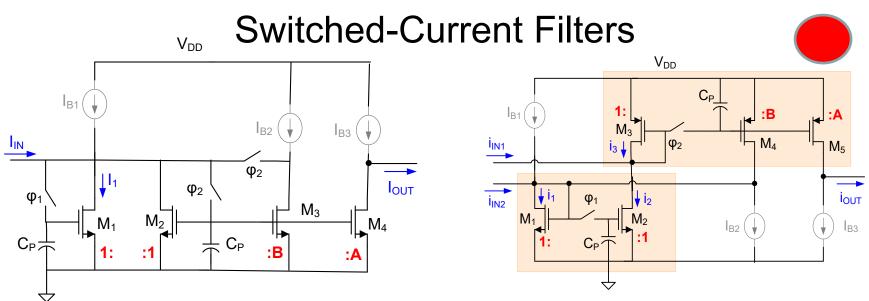
Backward Euler Lossy Inverting

$$G = \frac{A}{1+B} \qquad H = \frac{1}{1+B}$$

Another structure







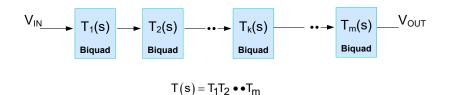
- Switched-current filters is an entirely different approach to designing filters with potential for overcoming many of the major problems facing the filter designer
- Other switched-current filter and integrator blocks have been proposed
- Integrators can be combined to form filter structures
- Single-ended and fully differential structures are readily formed
- Design of Switched-Current Filters is straightforward
- Beyond Hughes, a few others have looked at switched-current filters
- Hughes demonstrated experimentally modest performance with this technique
- Hughes was a world-class researcher and filter expert
- Hughes spent the better part of a decade trying to perfect the switched-current approach but performance remained modest when he retired
- Limited use of switched-current filters today

• Idea is really unique and there are bound to be some major useful applications of the basic concepts embodies in the switched-current filters!

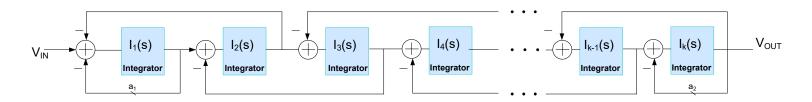


Filter Design/Synthesis Approaches

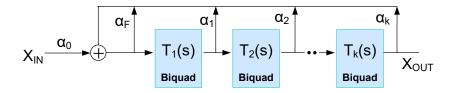
Cascaded Biquads



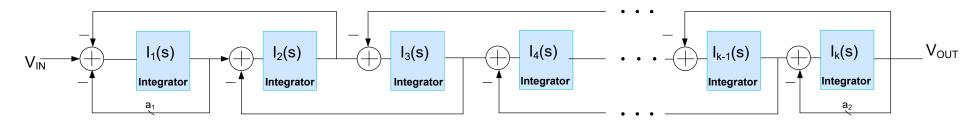
Leapfrog



Multiple-loop Feedback – One type shown



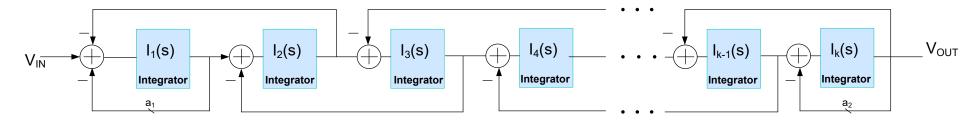
Leapfrog Filters



Introduced by Girling and Good, Wireless World, 1970

This structure has some very attractive properties and is widely used though the real benefits and limitations of the structure are often not articulated

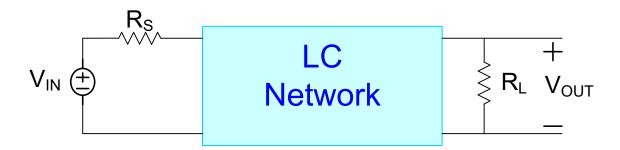
Leapfrog Filters



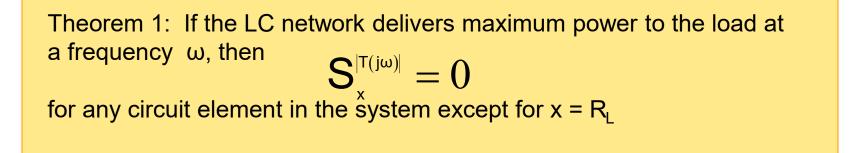
Observation: This structure appears to be dramatically different than anything else ever reported and it is not intuitive why this structure would serve as a filter, much less, have some unique and very attractive properties

To understand how the structure arose, why it has attractive properties, and to identify limitations, some mathematical background is necessary

Background Information for Leapfrog Filters

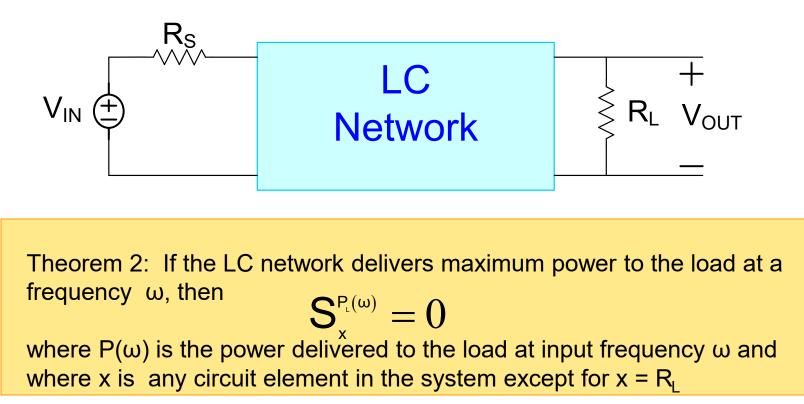


Assume the impedance R_S is fixed



This theorem will be easy to prove after we prove the following theorem:

Background Information for Leapfrog Filters

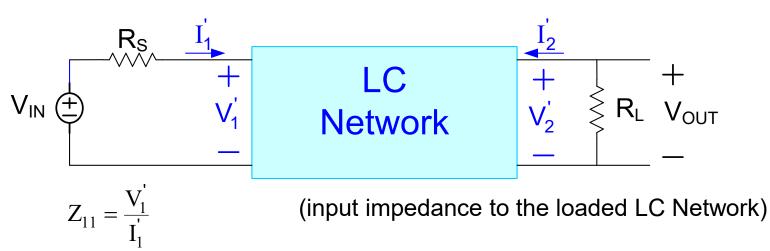


Note: There is no guarantee that there will be any frequencies where maximum power is transferred to the load and whether this does occur depends strongly on the LC circuit structure and the load R_L .

Proof of Theorem 2:

```
First, we will define the input impedance Z_{11}
```

Define the port phasors as $\{V'_1, I'_1, V'_2, I'_2\}$



this can be expressed as

 $Z_{11} = R_1 + jX_1 \qquad \qquad (\mathsf{R_1} \text{ and } \mathsf{X_1} \text{ are real functions of } \omega \text{ and depend on } \mathsf{R_L})$

Since the LC network is lossless (dissipates no power) we have

$$\begin{split} P_{L} &= \mathsf{Re}\Big(V_{1}^{'} \bullet I_{1}^{'*}\Big) \\ P_{L} &= \mathsf{Re}\Bigg(\bigg[\frac{R_{1} + jX_{1}}{R_{S} + R_{1} + jX_{1}}V_{in}\bigg] \bullet \bigg[\frac{V_{in}}{R_{S} + R_{1} + jX_{1}}\bigg]^{*}\Bigg) \\ P_{L} &= |V_{in}|^{2}\,\mathsf{Re}\Bigg(\frac{R_{1} + jX_{1}}{\left(R_{S} + R_{1}\right)^{2} + X_{1}^{2}}\Bigg) = |V_{in}|^{2}\frac{R_{1}}{\left(R_{S} + R_{1}\right)^{2} + X_{1}^{2}} \end{split}$$

Proof of Theorem 2:

$$P_{L} = |V_{in}|^{2} \frac{R_{1}}{(R_{s} + R_{1})^{2} + X_{1}^{2}}$$

To maximize power delivered to a fixed load at a frequency ω , must have

Proof of Theorem 2:

$$X_{1} = 0$$
 (1) $R_{1} = R_{S}$ (2)
 $P_{L} = |V_{in}|^{2} \frac{R_{1}}{(R_{S} + R_{1})^{2} + X_{1}^{2}}$

Now let x be any element in the LC network

$$\frac{\partial \mathbf{P}_{\mathrm{L}}}{\partial \mathbf{X}} = \frac{\partial \mathbf{P}_{\mathrm{L}}}{\partial \mathbf{R}_{\mathrm{1}}} \frac{\partial \mathbf{R}_{\mathrm{1}}}{\partial \mathbf{X}} + \frac{\partial \mathbf{P}_{\mathrm{L}}}{\partial \mathbf{X}_{\mathrm{1}}} \frac{\partial \mathbf{X}_{\mathrm{1}}}{\partial \mathbf{X}}$$
$$\frac{\partial \mathbf{P}_{\mathrm{L}}}{\partial \mathbf{X}} = \left[\left| \mathbf{V}_{\mathrm{in}} \right|^{2} \left[\frac{\left(2\left(\mathbf{R}_{\mathrm{S}}^{2} - \mathbf{R}_{\mathrm{1}}^{2}\right) + \mathbf{X}_{\mathrm{1}}^{2}\right)}{\left(\left(\mathbf{R}_{\mathrm{S}} + \mathbf{R}_{\mathrm{1}}\right)^{2} + \mathbf{X}_{\mathrm{1}}^{2}\right)^{2}} \right] \right] \frac{\partial \mathbf{R}_{\mathrm{1}}}{\partial \mathbf{X}} + \left[\left| \mathbf{V}_{\mathrm{in}} \right|^{2} \left[\frac{-\mathbf{R}_{\mathrm{1}}\left(2\mathbf{X}_{\mathrm{1}}\right)}{\left(\left(\mathbf{R}_{\mathrm{S}} + \mathbf{R}_{\mathrm{1}}\right)^{2} + \mathbf{X}_{\mathrm{1}}^{2}\right)^{2}} \right] \right] \frac{\partial \mathbf{X}_{\mathrm{1}}}{\partial \mathbf{X}}$$

It thus follows from (1) and (2) that at maximum power transfer, the two coefficients in this expression vanish, thus

$$\frac{\partial P_{L}}{\partial x} = \left[\left| V_{in} \right|^{2} \left[\frac{0}{\left(\left(R_{s} + R_{1} \right)^{2} + X_{1}^{2} \right)^{2}} \right] \right] \frac{\partial R_{1}}{\partial x} + \left[\left| V_{in} \right|^{2} \left[\frac{0}{\left(\left(R_{s} + R_{1} \right)^{2} + X_{1}^{2} \right)^{2}} \right] \right] \frac{\partial X_{1}}{\partial x} = 0$$

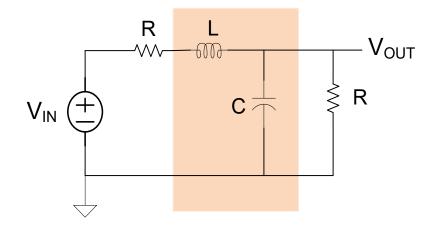
thus

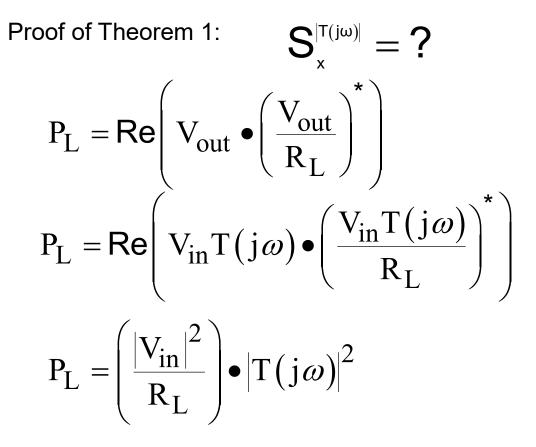
$$S_{x}^{P_{L}} = \frac{\partial P_{L}}{\partial x} \frac{x}{P_{L}} = 0$$

Question: Can we also make the claim that $S_{R}^{P_{c}(\omega)} = 0$ at any frequency where maximum power is transferred to the load?

Yes! Note that the previous analysis is based upon characterizing R_1 and X which are functions of k reactive components, $\{x_1, \dots, x_k\}$ and R_L

The following circuit has maximum power transfer at dc and it can be easily analytically shown that the sensitivity of P to L, C, and R_L is 0 at dc.





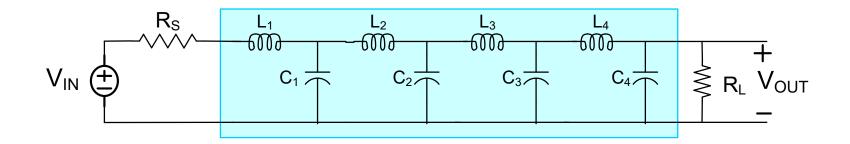
Recall the following two sensitivity relationships

$$S_x^{kf} = S_x^f \qquad S_x^{f^2} = 2 \bullet S_x^f$$

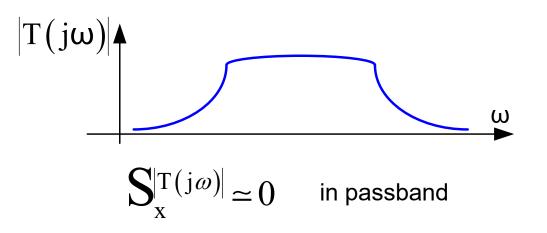
It thus follows that

$$S_{x}^{P_{L}} = 2 \bullet S_{x}^{|T(j\omega)|} \qquad S_{x}^{P_{L}} = 0 \qquad \qquad S_{x}^{|T(j\omega)|} = 0$$

Many passive LC filters such as that shown below exist that have near maximum power transfer in the passband

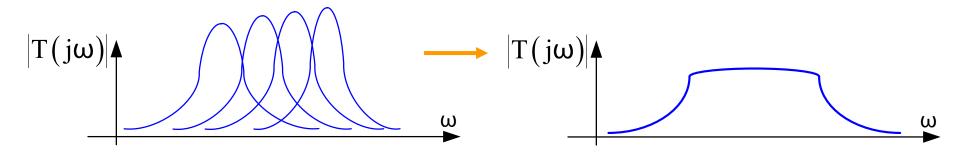


If a component in the LC network changes a little, there is little change in the passband gain characteristics (depicted as bandpass)

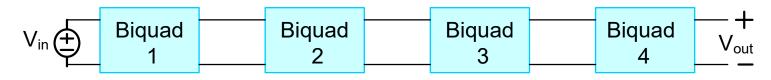




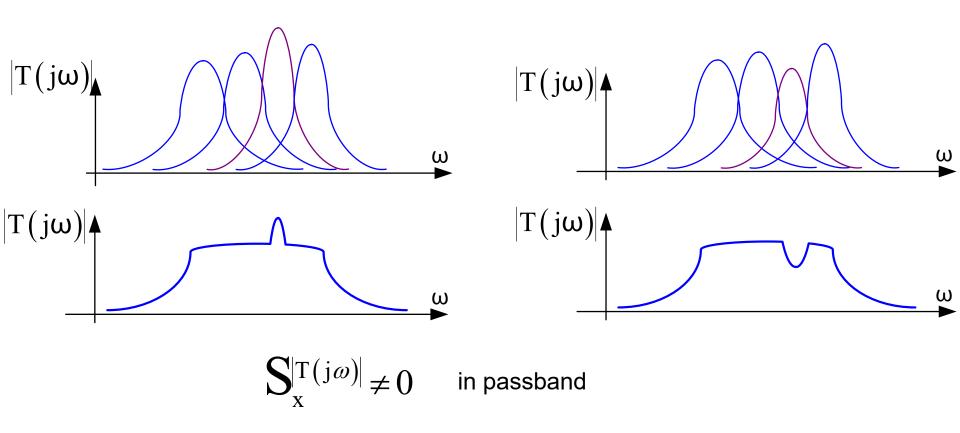
Cascaded Biquad has a response that is the product of the individual second-order transfer functions

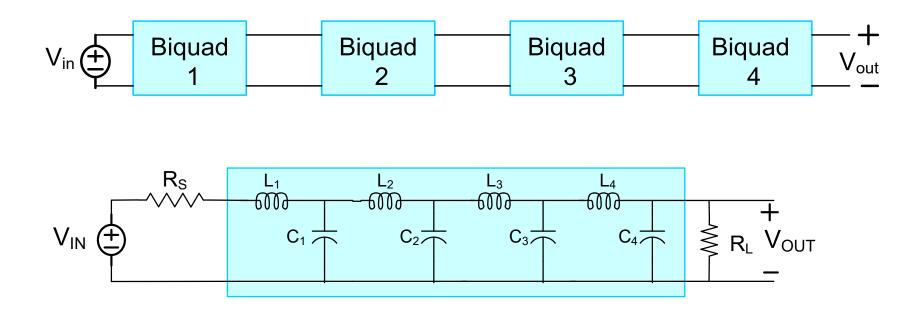


If a component in a biquad changes a little, there is often a large change in the passband gain characteristics (depicted as bandpass)



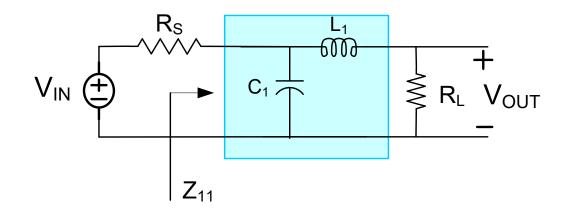
If a component in a biquad changes a little, there is often a large change in the passband gain characteristics (depicted as bandpass)





Good doubly-terminated LC networks often much less sensitive to most component values in the passband than are cascaded biquads !

This is a major advantage of the LC networks but can not be applied practically in most integrated applications or even in pc-board based designs Example: Determine at what frequencies maximum-power transfer to the load will occur and what value of R_L is needed for this to happen



Recall at maximum-power transfer, Z_{11} is real and equal to R_s

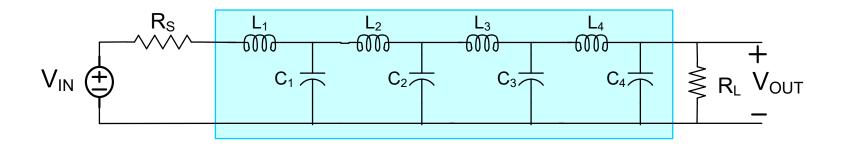
$$Z_{11} = \frac{R_{L} + sL}{s^{2}LC + sR_{L}C + 1}$$

$$Z_{11} (j\omega) = \left(\frac{R_{L}}{(1 - \omega^{2}LC)^{2} + \omega^{2}R_{L}^{2}C}\right) + j\left(\frac{\omega L - \omega^{2}R_{L}^{2}C - \omega^{3}L^{2}C}{(1 - \omega^{2}LC)^{2} + \omega^{2}R_{L}^{2}C}\right)$$

$$Im(Z_{11} (j\omega)) = 0 \quad \text{only at} \quad \omega = 0 \text{ and one other positive value of } \omega$$
To get maximum power transfer at $\omega = 0$, must have $R_{L} = R_{S}$

Appears not to have maximum power transfer at other frequency where $Im(Z_{11}(j\omega)) \neq 0$

Consider again the doubly-terminated circuit that has multiple passband frequencies where maximum power transfer to the load occurs

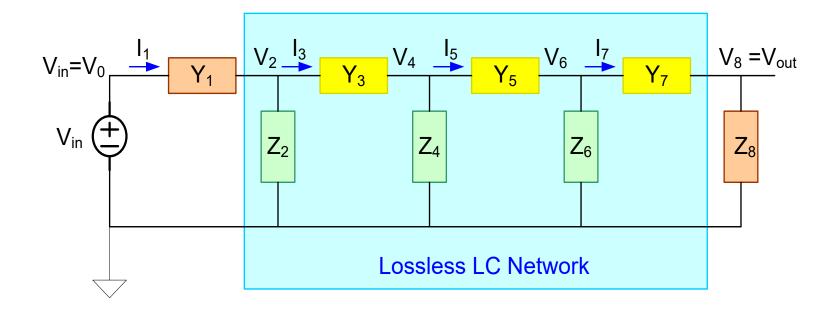


Observe that this structure is completely characterized by a set of equations that characterize the network

All sensitivity properties are inherently determined by this set of equations

Any circuit that has the same set of equations will have the same sensitivity properties

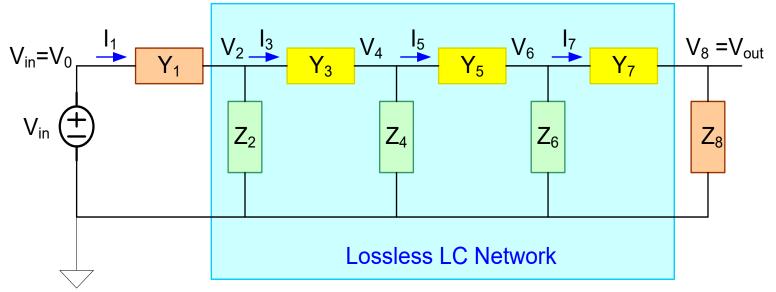
Doubly-terminated Ladder Network with Low Passband Sensitivities



For components in the LC Network observe

$$Y_k = \frac{1}{sL_k} \qquad \qquad Z_k = \frac{1}{sC_k}$$

Doubly-terminated Ladder Network with Low Passband Sensitivities



$$I_{1} = (V_{0} - V_{2})Y_{1}$$

$$V_{2} = (I_{1} - I_{3})Z_{2}$$

$$I_{3} = (V_{2} - V_{4})Y_{3}$$

$$V_{4} = (I_{3} - I_{5})Z_{4}$$

$$I_{5} = (V_{4} - V_{6})Y_{5}$$

$$V_{6} = (I_{5} - I_{7})Z_{6}$$

$$I_{7} = (V_{6} - V_{8})Y_{7}$$

$$V_{8} = I_{7}Z_{8}$$

Complete set of independent equations that characterize this filter

Solution of this set of equations is tedious

All sensitivity properties of this circuit are inherently embedded in these equations!



Stay Safe and Stay Healthy !

End of Lecture 30